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Docket No.: 060188-0075

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Customer Number: 20277
FUJIWARA, MAKOTO : Confirmation Number: 5700
Application No.: 09/867,766 : Group Art Unit: 2134
Filed: May 31, 2001 : Examiner: Poltorak, Piotr
For: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF TESTING
SEMICONDUCTOR INTEGRATED CIRCUIT

PETITION FOR EXTENSION OF TIME

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time for response to the Office Action dated July 1, 2005, now due to expire October 1, 2005, be extended for one month(s) and set to expire on November 1, 2005.

Please charge the extension fee of \$120.00 to Deposit Account No. 500417. Please charge any additional fees or credit any overpayment to Deposit Account No. 500417.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


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Date: November 1, 2005

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Please recognize our Customer No. 20277
as our correspondence address.

11/03/2005 MBINAS 00000013 500417 09867766
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